In the Specification

Page 1, before the first line, insert: *, now US patent 6,766,831,*

This is a continuation of application Serial Number 10/114,652 filed on April 1, 2002

which is a continuation of application Serial Number 09/238,446 filed on January 28, 1999, now

U.S. Patent No. 6,366,999, which claims the benefit of Provisional Serial No. 60/072,915 filed

January 28, 1998, each of which is incorporated by reference herein in its entirety.

Replace the paragraph beginning at page 6, line 10, with the following rewritten

paragraph:

It is desirable to have an efficient mechanism for or means to generate complex

conditions in each PE that can be specified and tested for by conditional instructions. This has

the effect of changing SP conditional branches into PE data dependent execution operations.

Having an effective means for parallel array conditional execution minimizes the need to have

the PEs send condition signals back to the controller, which takes time and implementation

expense, for the purposes of supporting conditional branching based on PE conditions. An

implication of having parallel array conditional execution is that the approach chosen for

providing PE condition feedback to the array controller can be simple in nature and less costly

than providing condition signaling paths from each PE. By saving the condition flags in a

programmer accessible register space that can be copied or moved to a PE's register file the flags

can be easily communicated between PEs. In conjunction with a merged SP/PE as described

more fully in U.S. Application Serial No. 09/169,072 filed October 9, 1998 entitled Methods and

Apparatus for Dynamically Merging an Array Controller with an Array Processor Element, flags